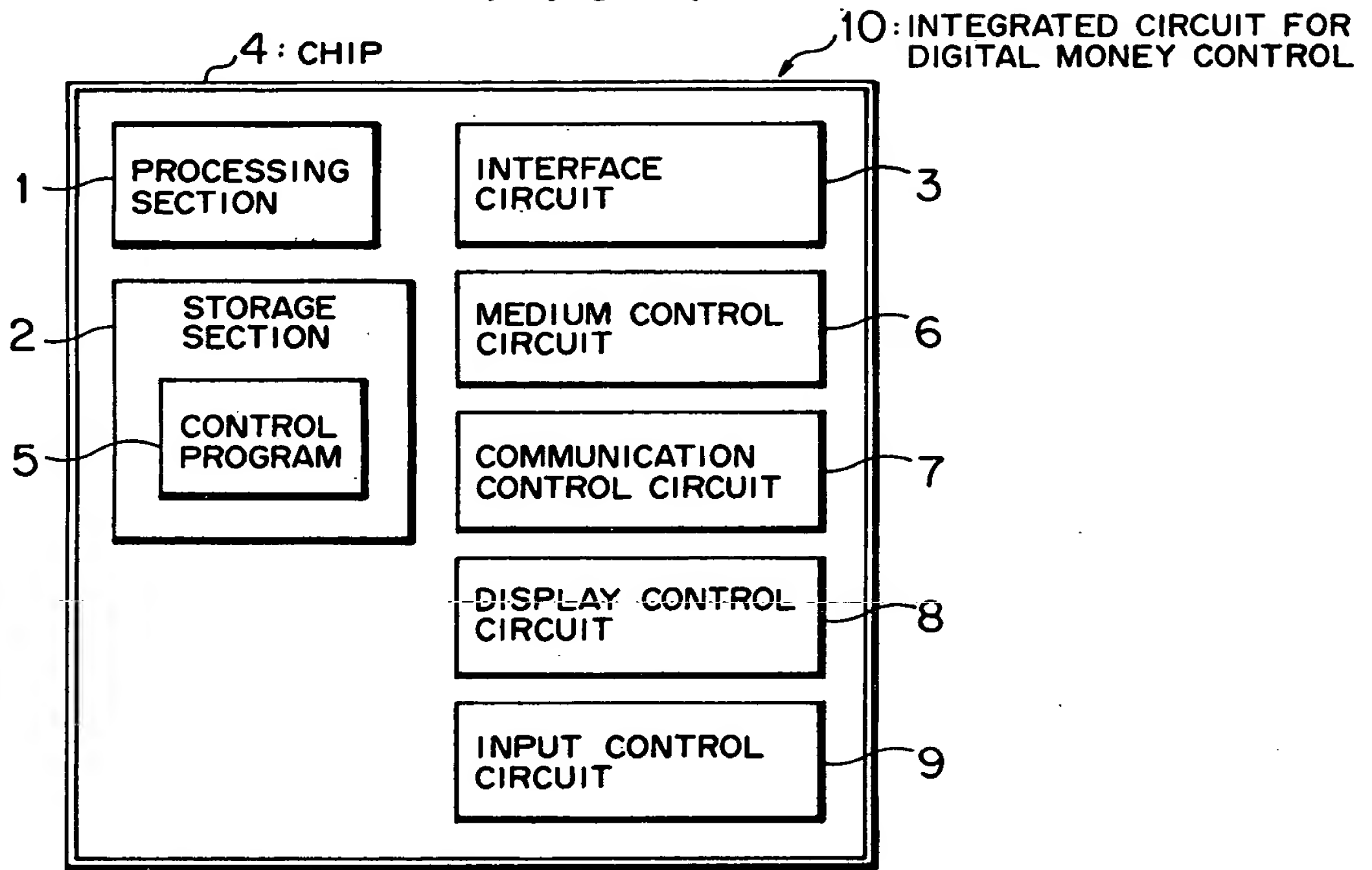
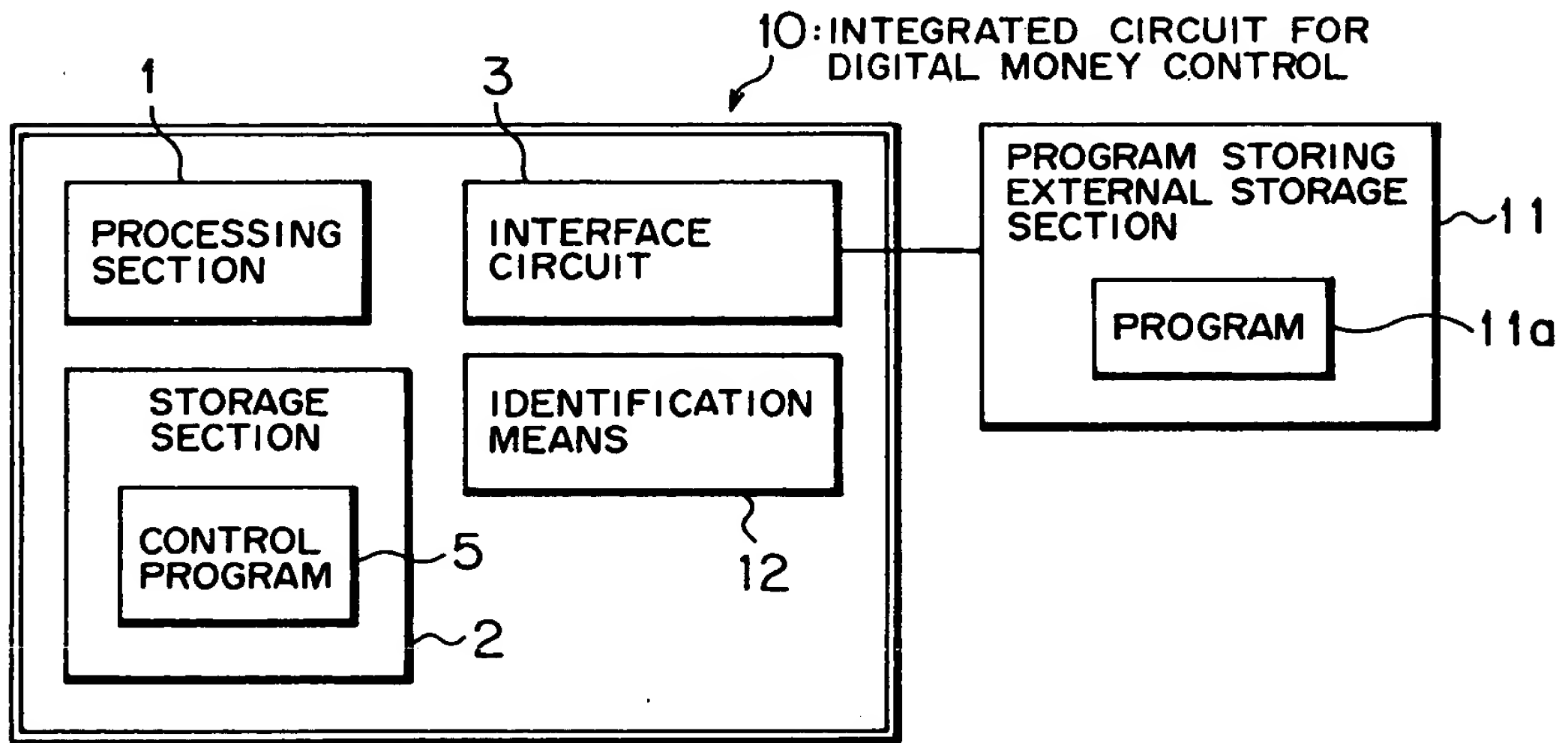


APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

# FIG. 1



# FIG. 2



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 3

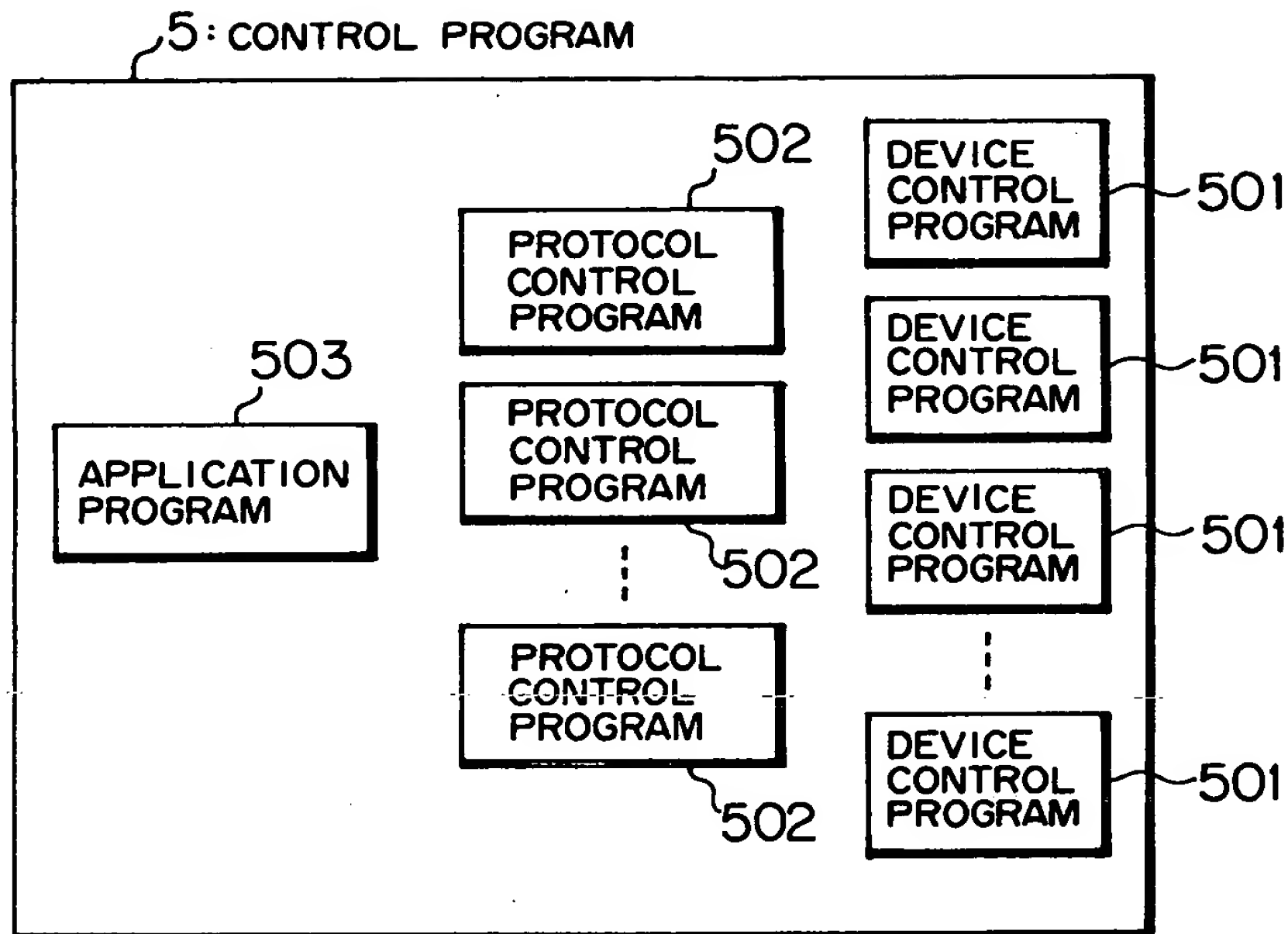


FIG. 4

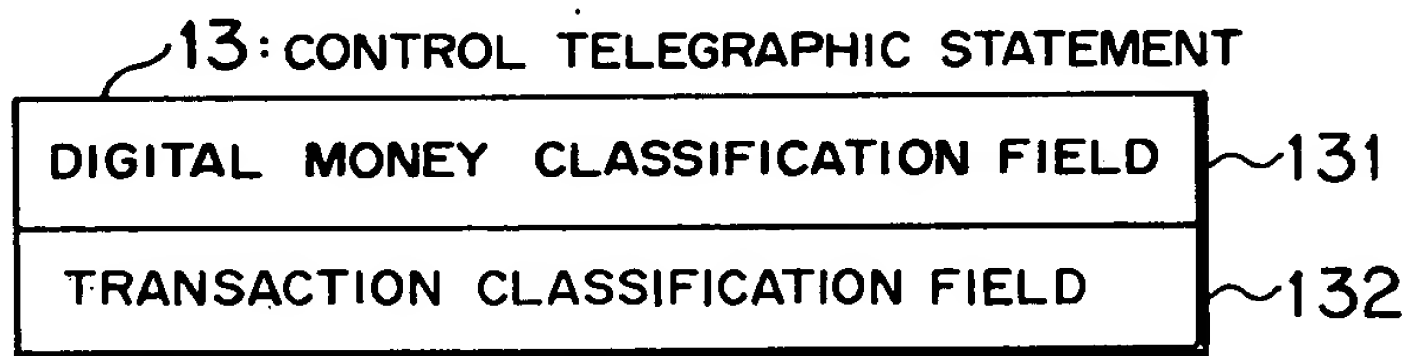


FIG. 5

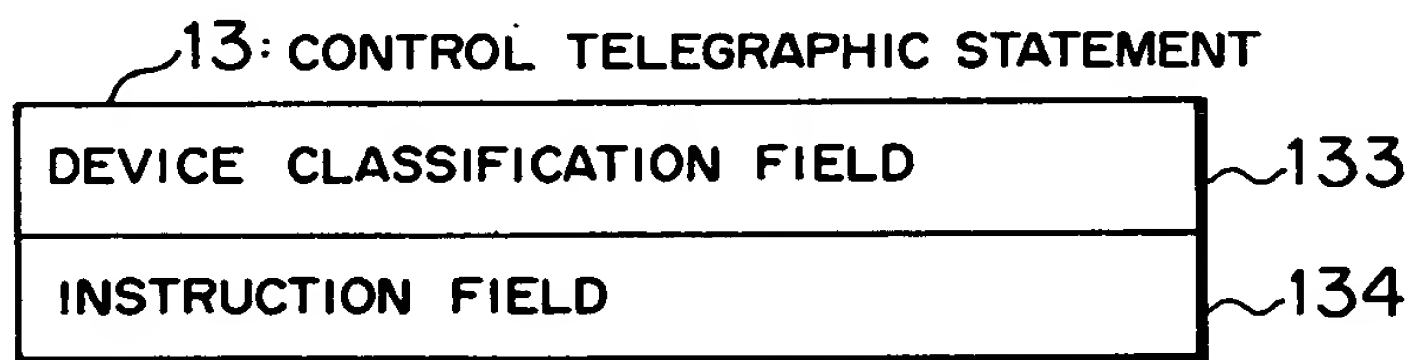
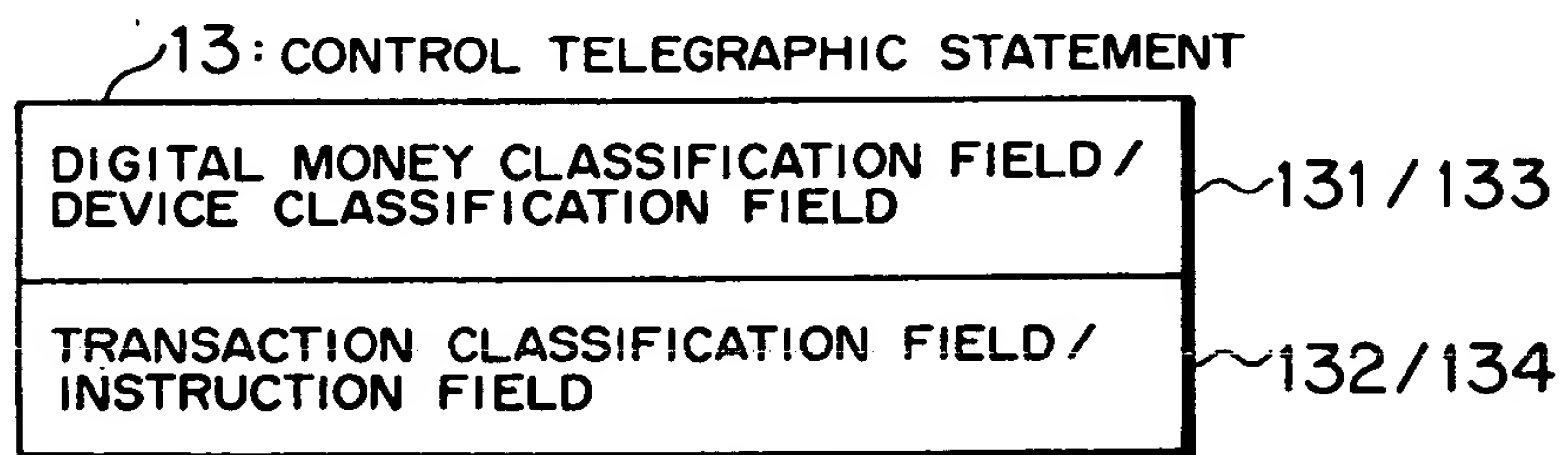
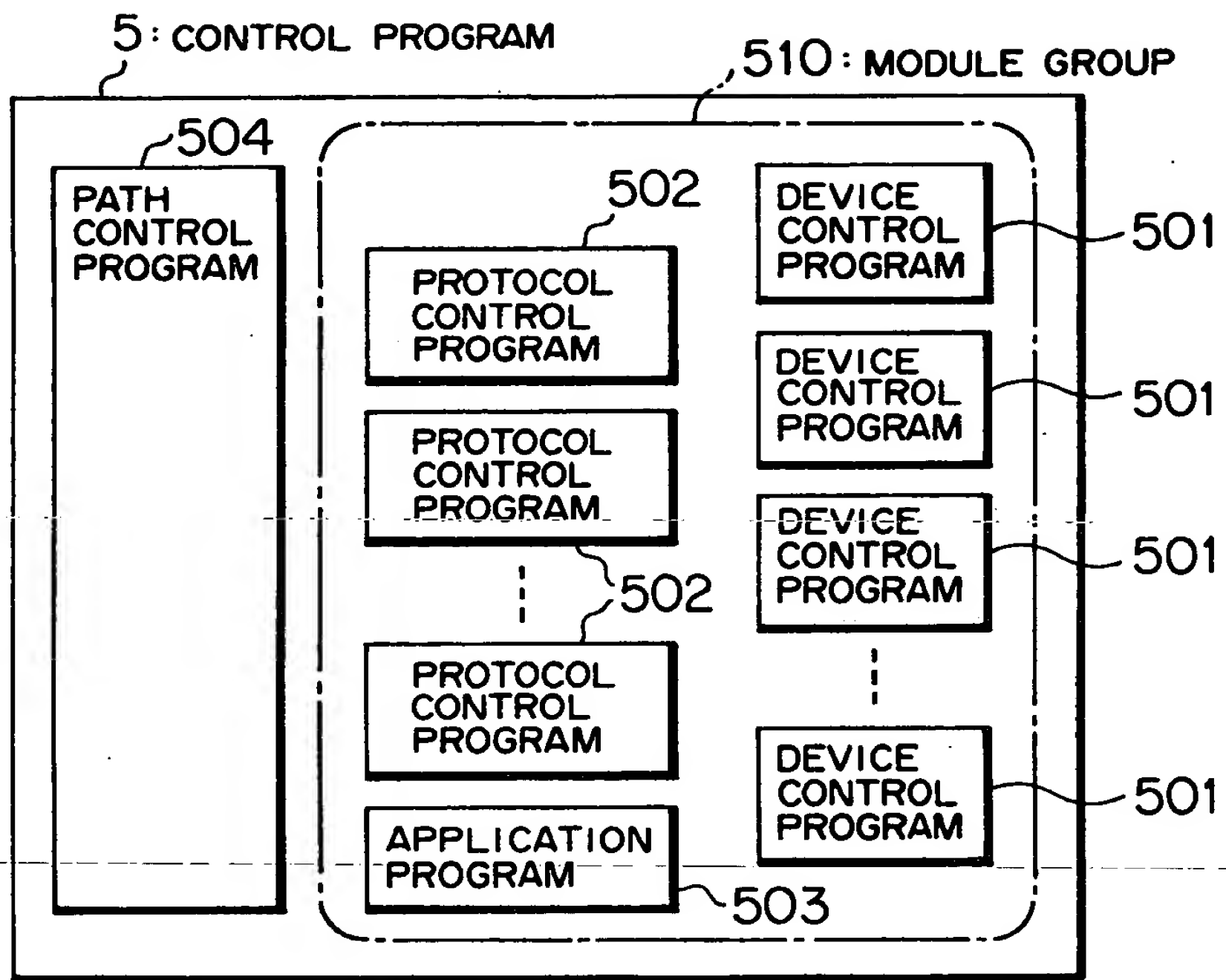


FIG. 6



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

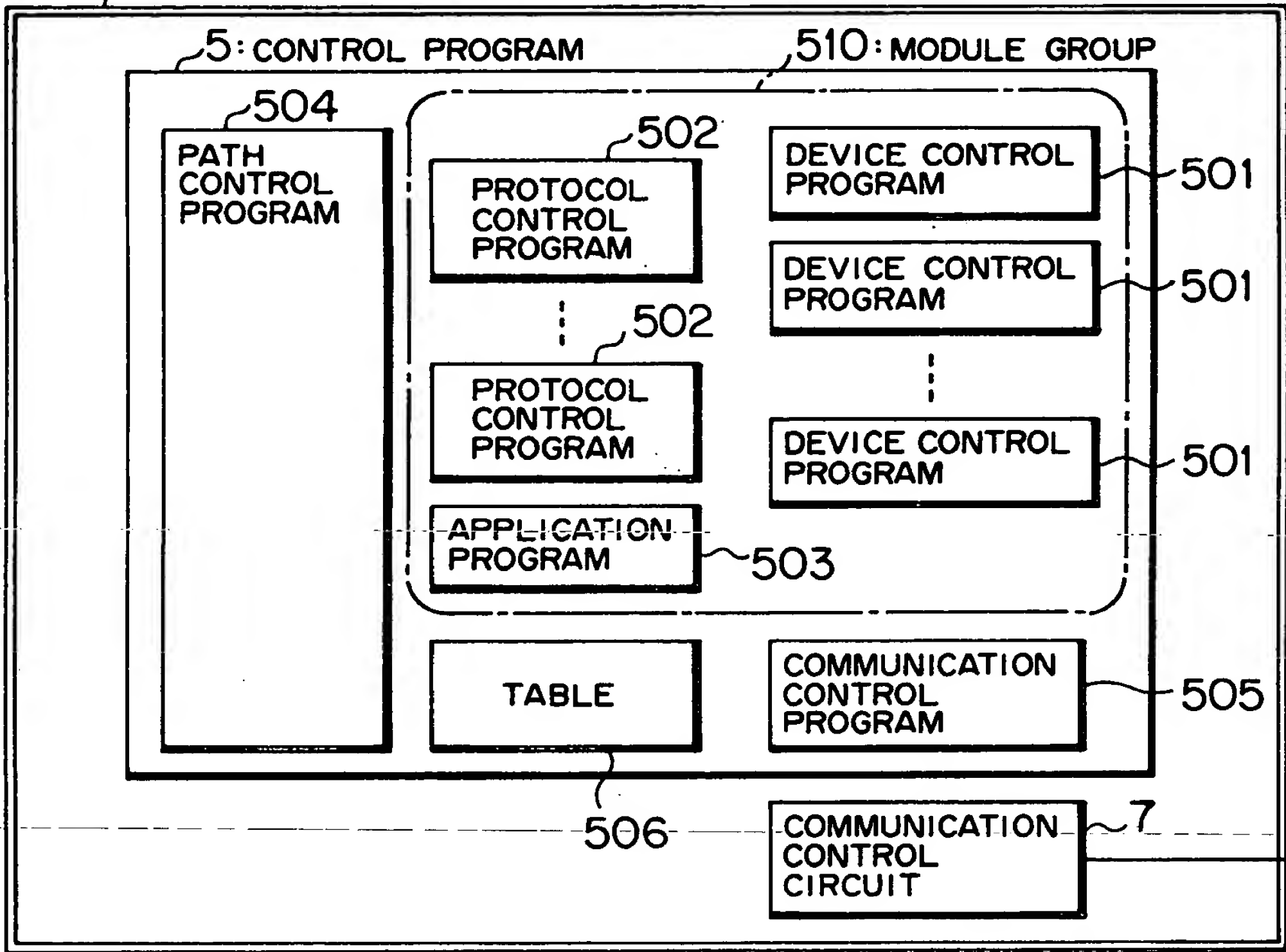
FIG. 7



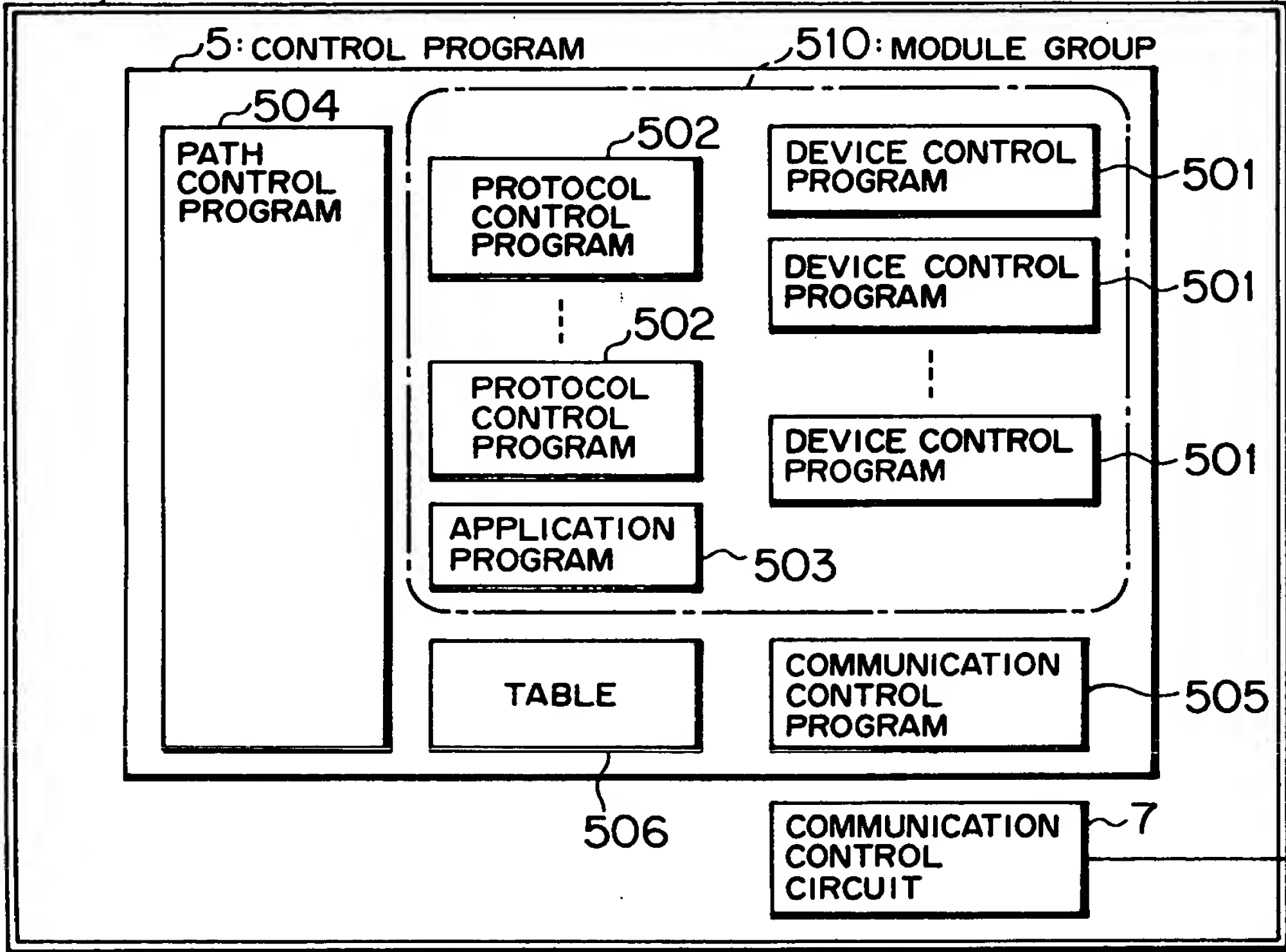


# FIG. 9

10: INTEGRATED CIRCUIT FOR DIGITAL MONEY CONTROL



10: INTEGRATED CIRCUIT FOR DIGITAL MONEY CONTROL (14: EXTERNAL UNIT)



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 10

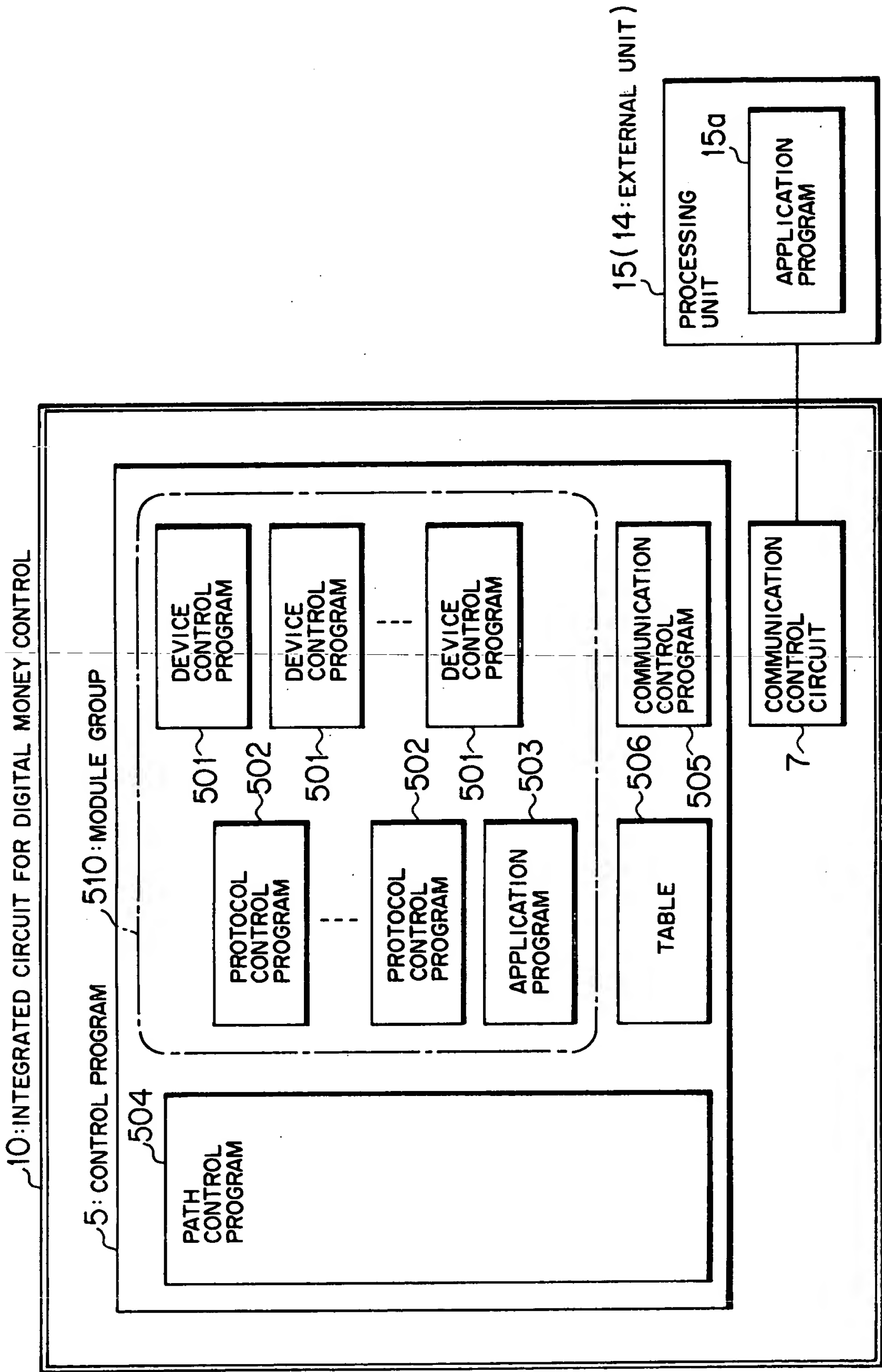
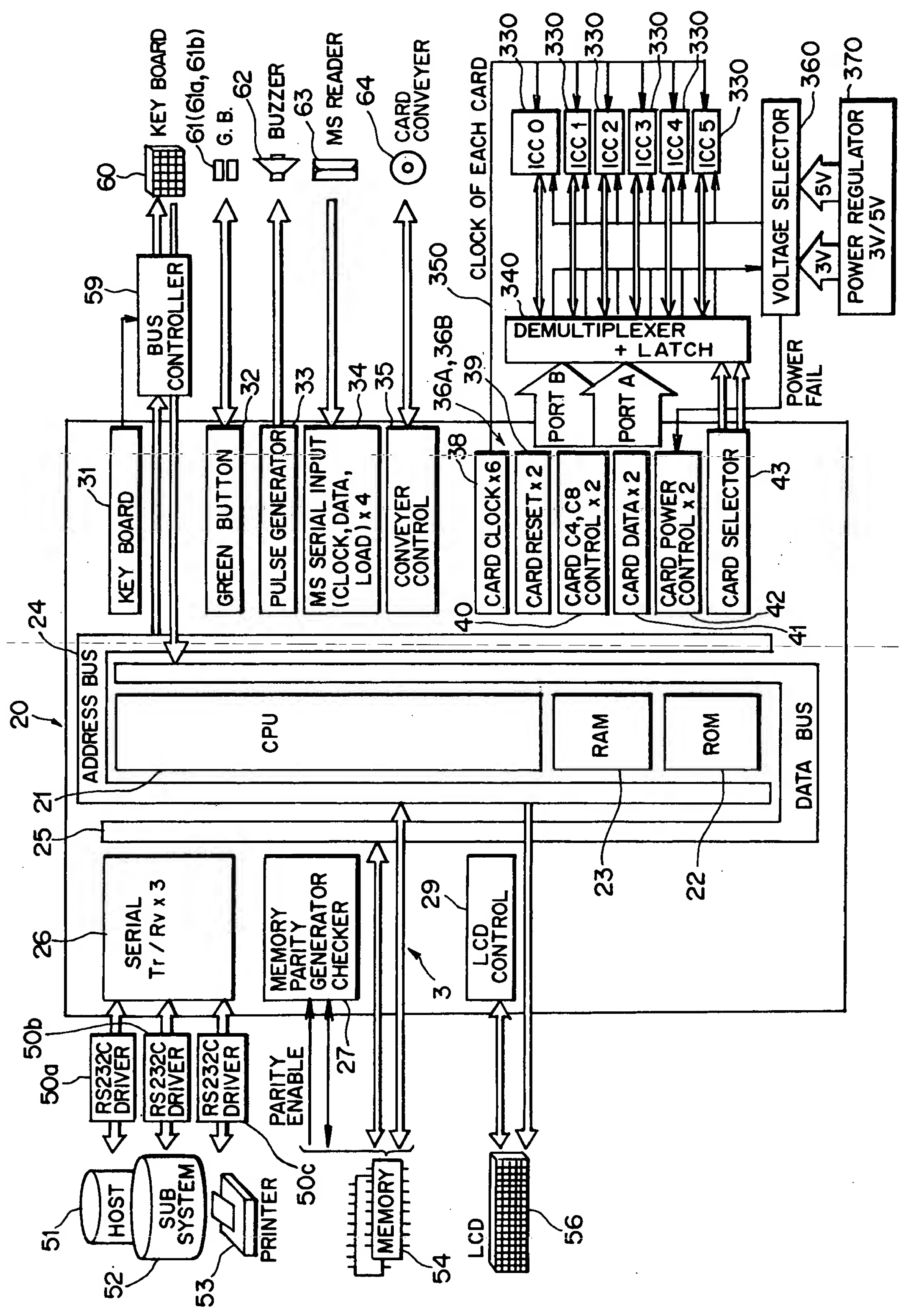
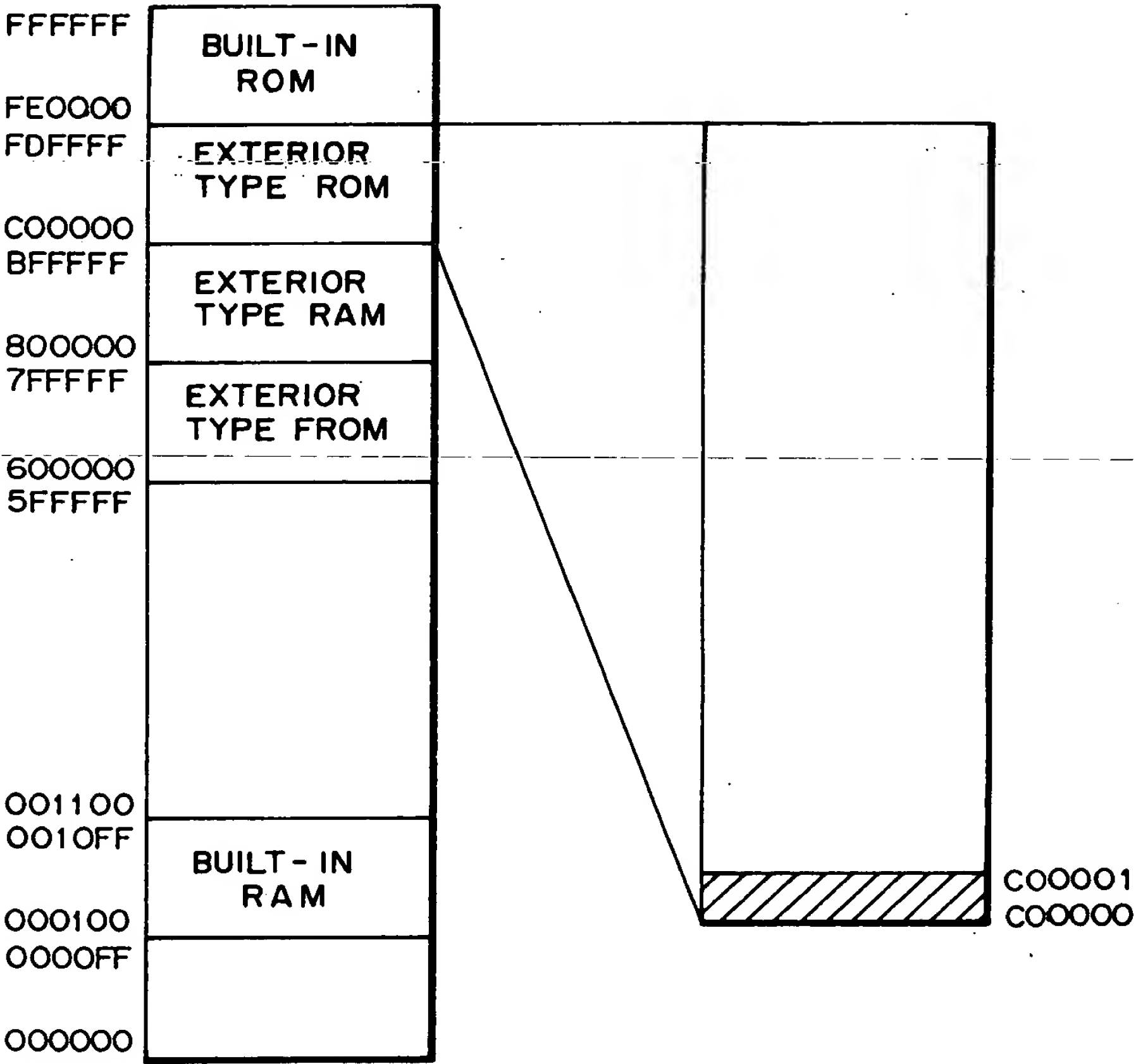


FIG. 11



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

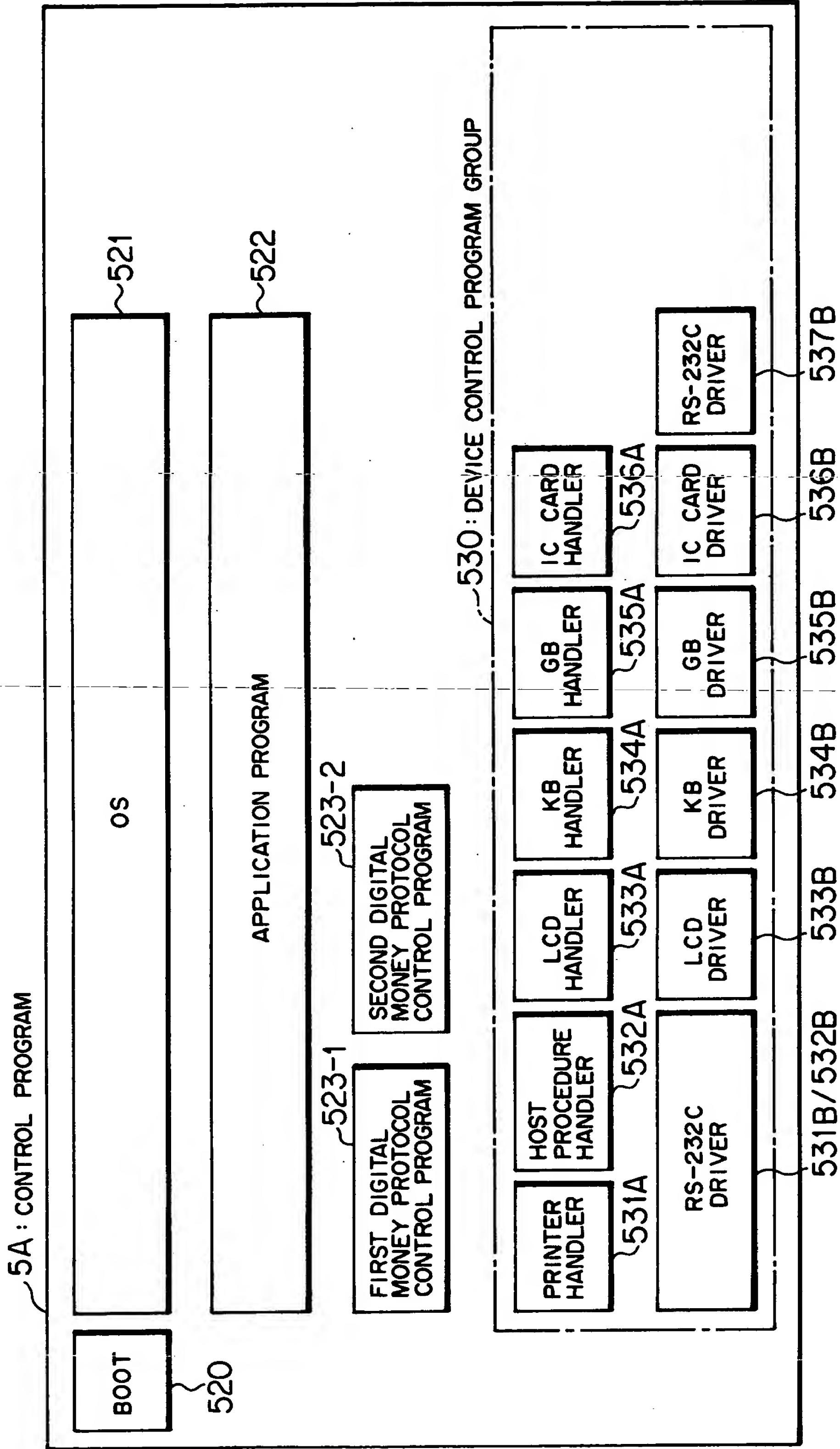
FIG. 12



602600 001000



# FIG. 13









APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 18

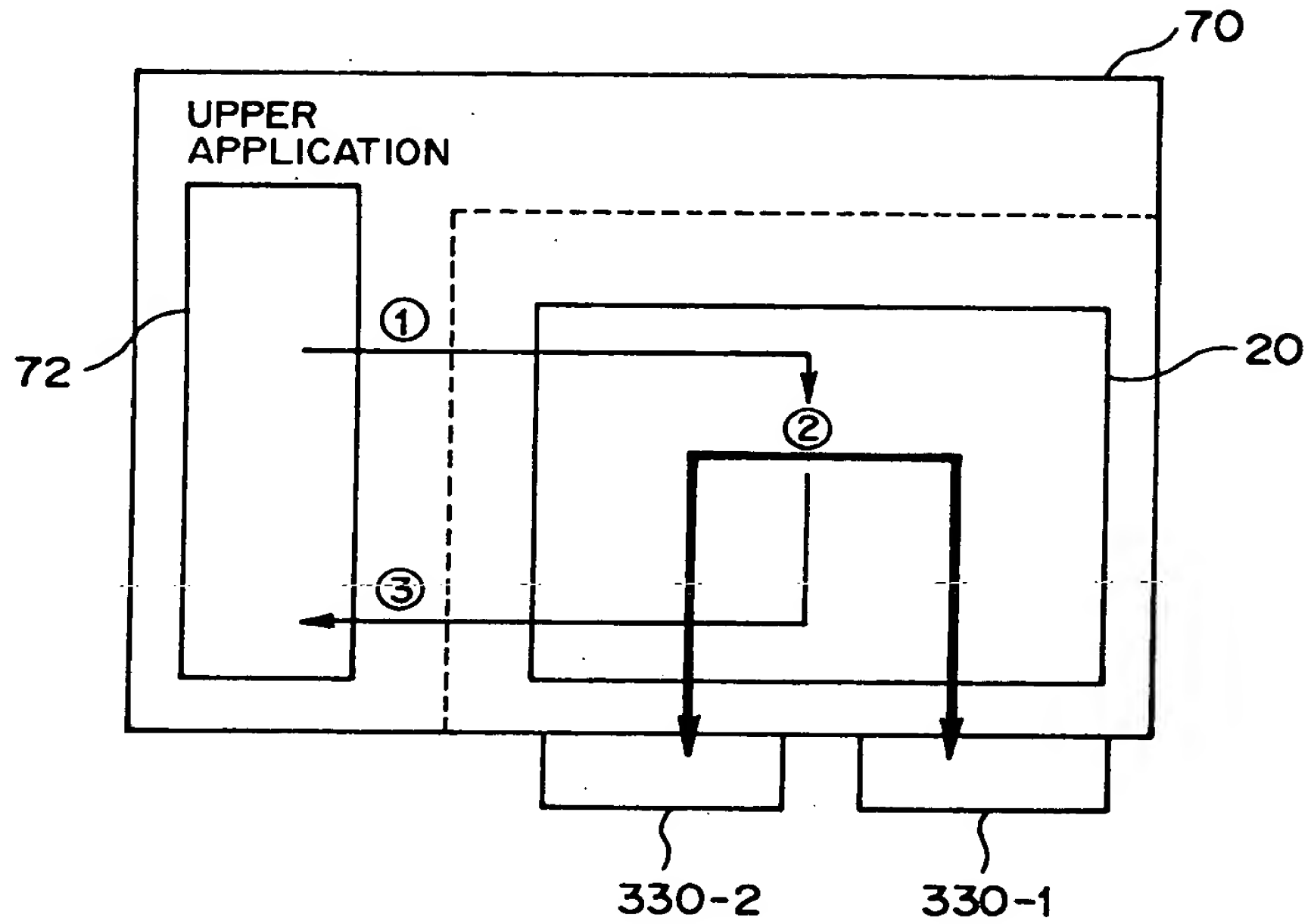


FIG. 19

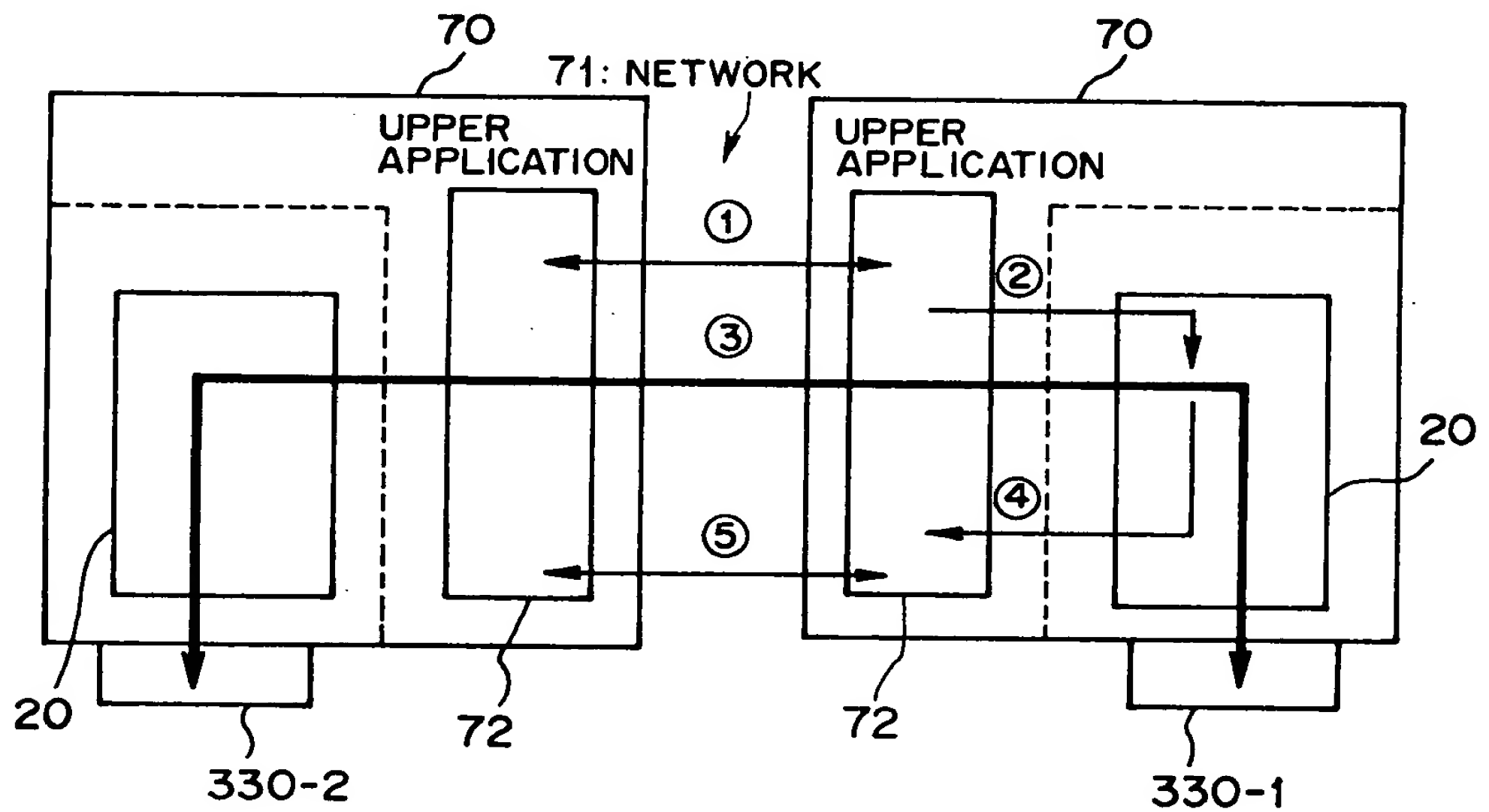


FIG. 20

FIG. 20

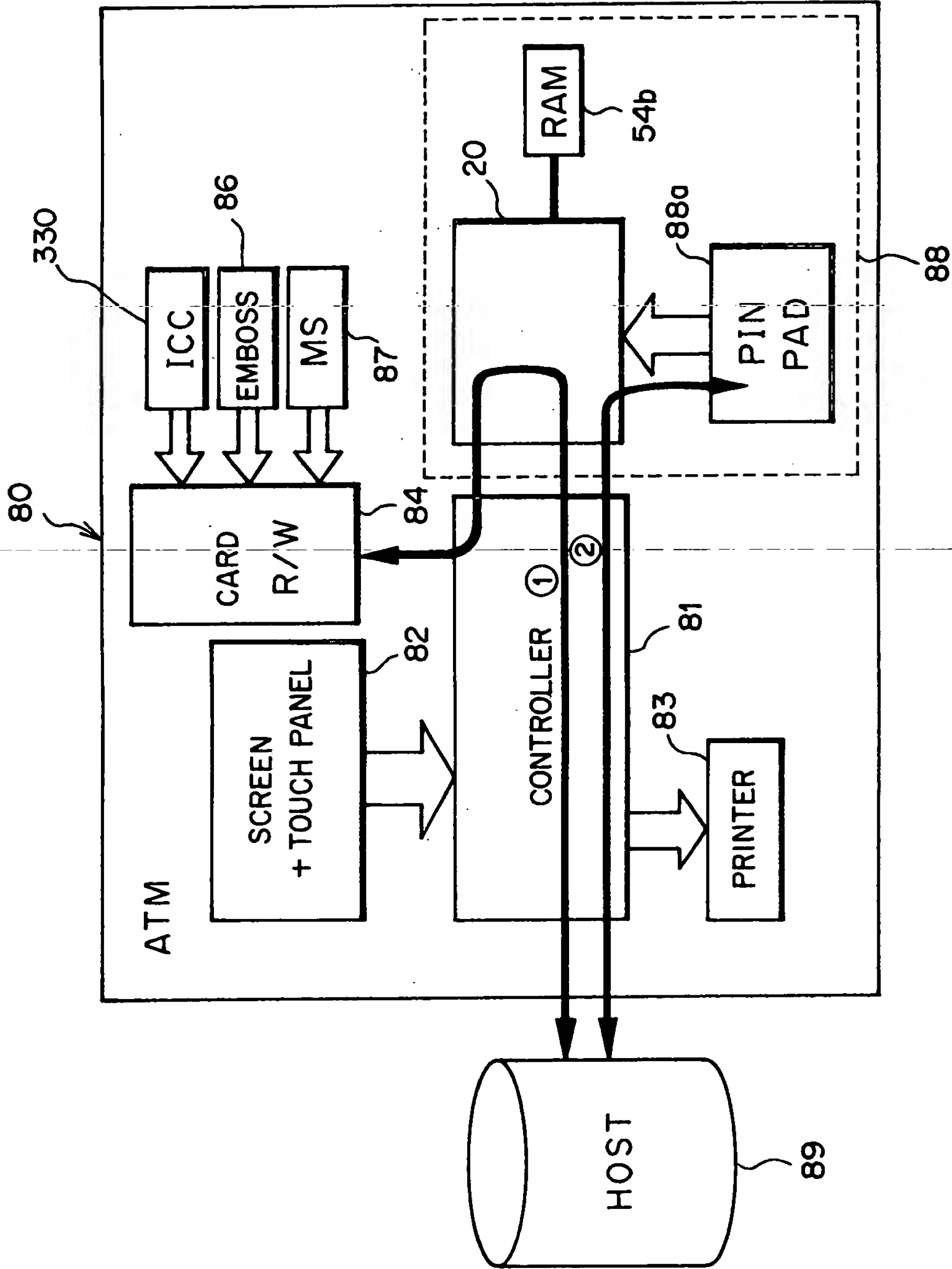


FIG. 21

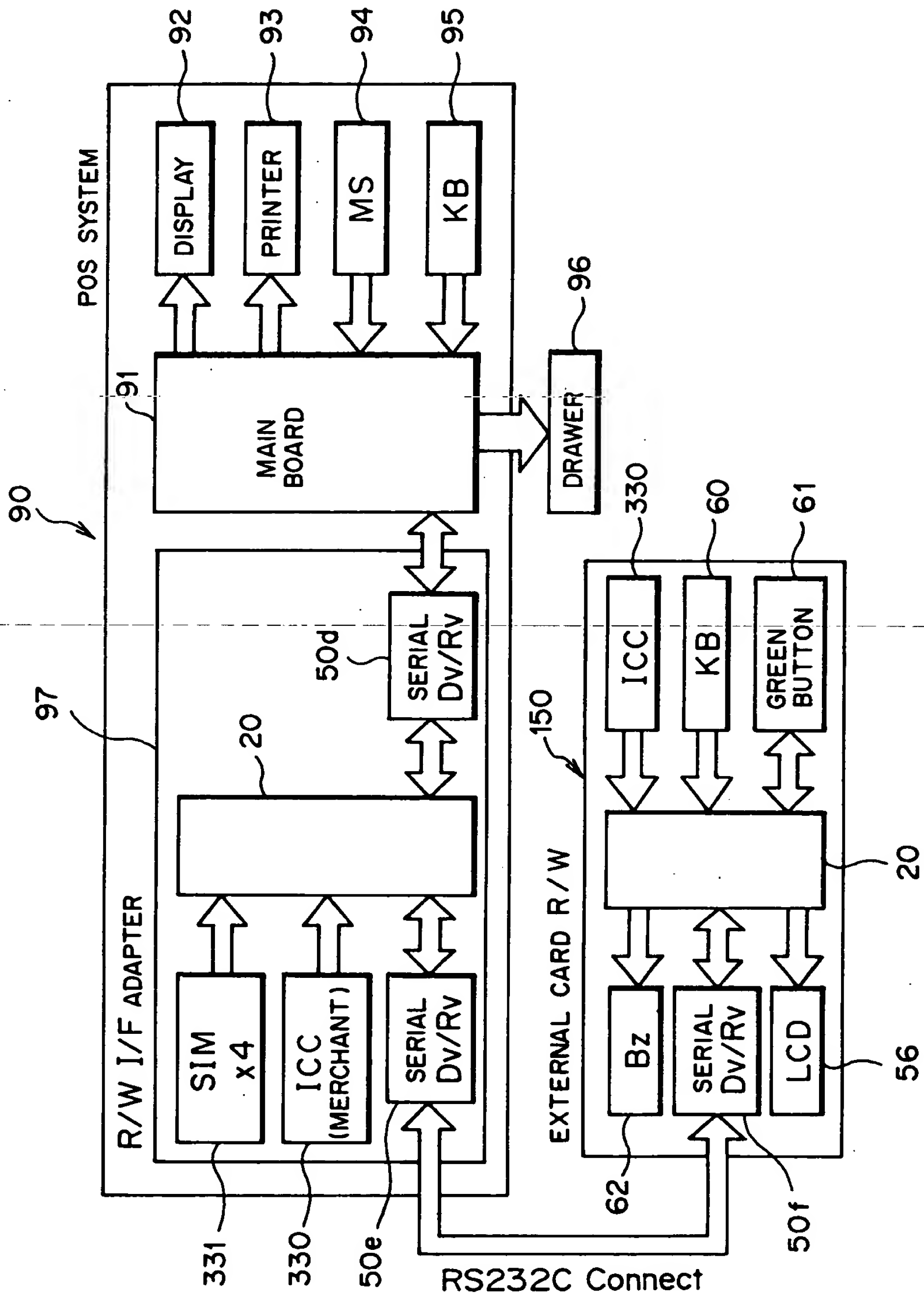
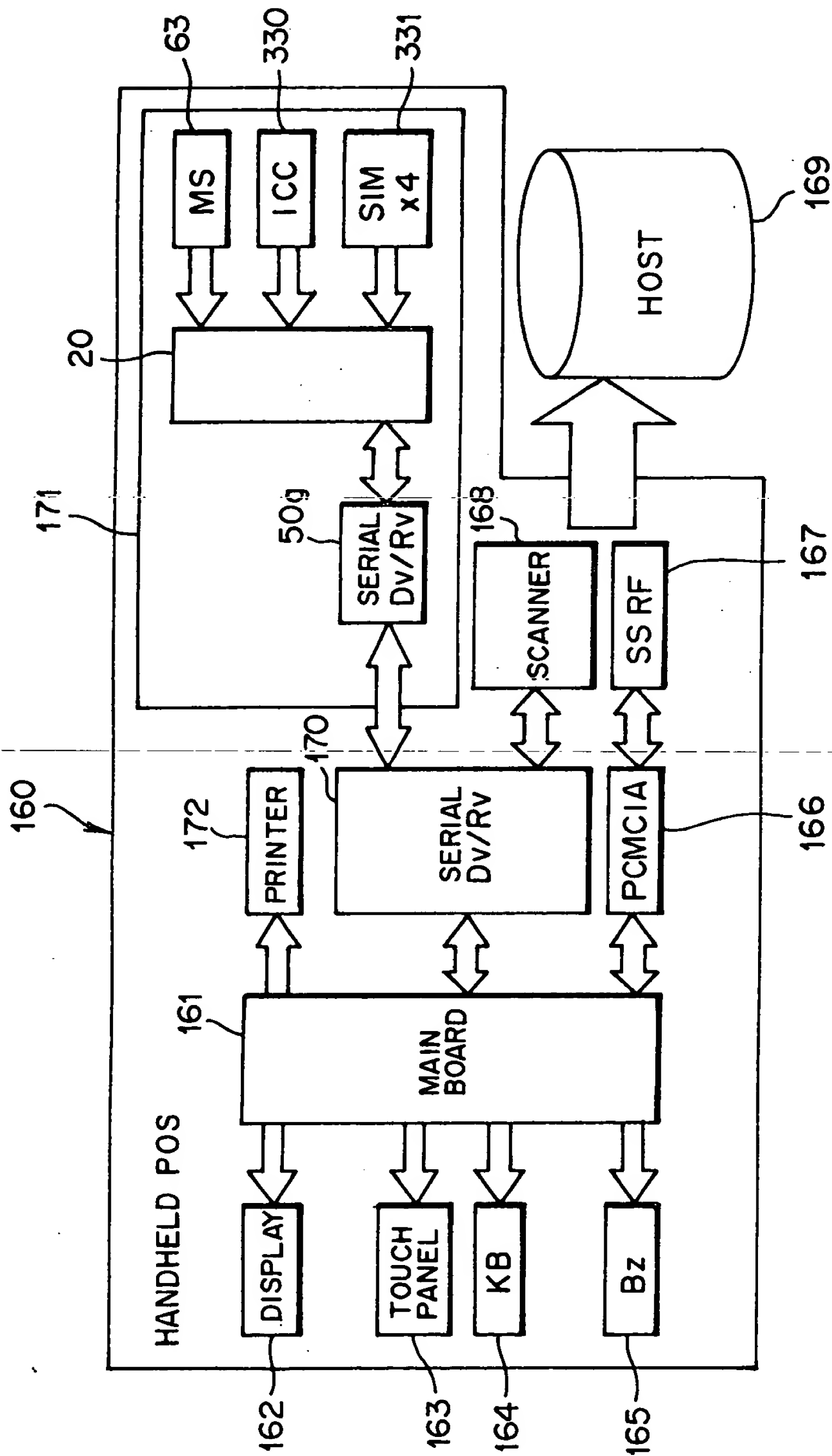


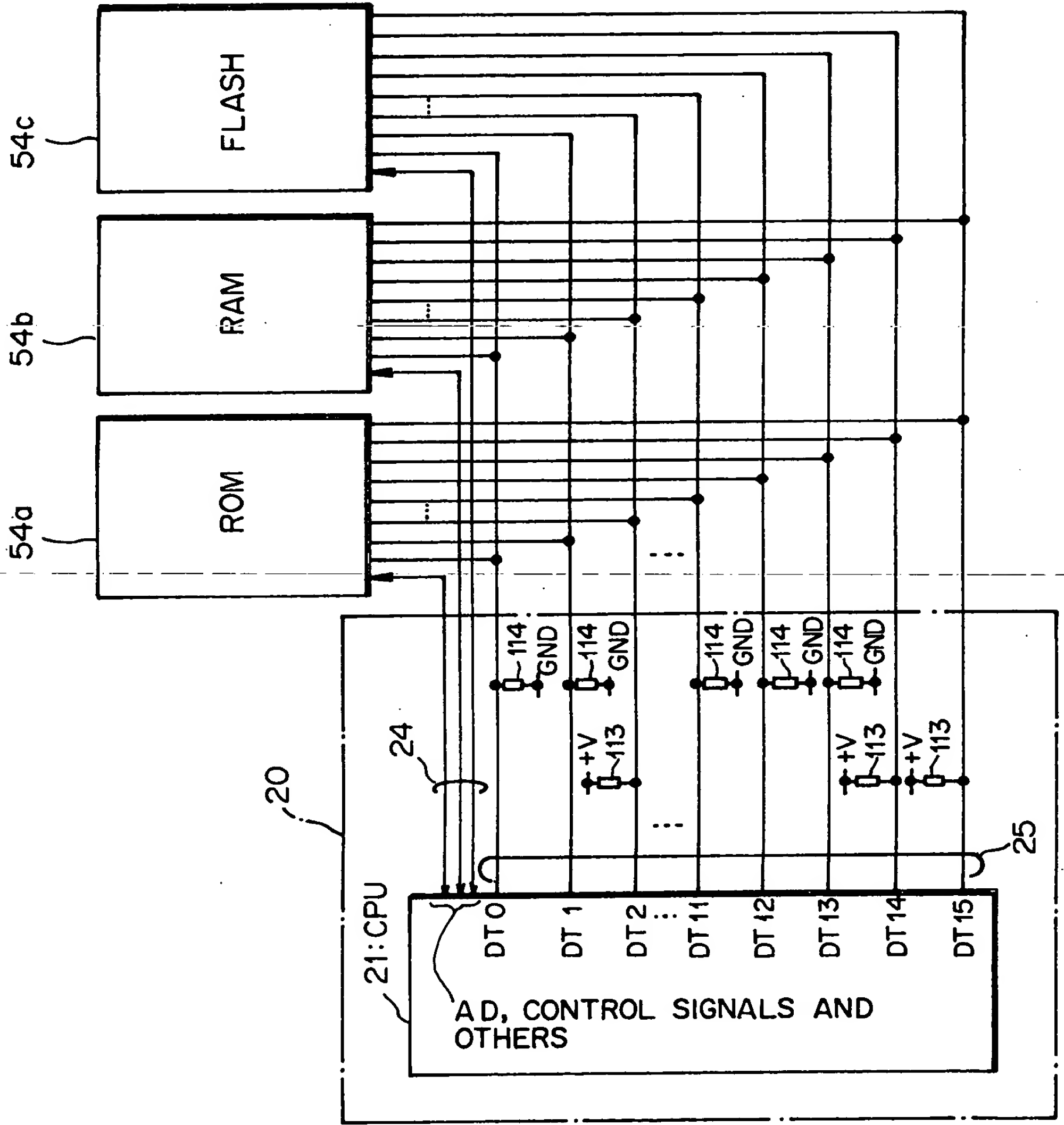
FIG. 22





668730-309260

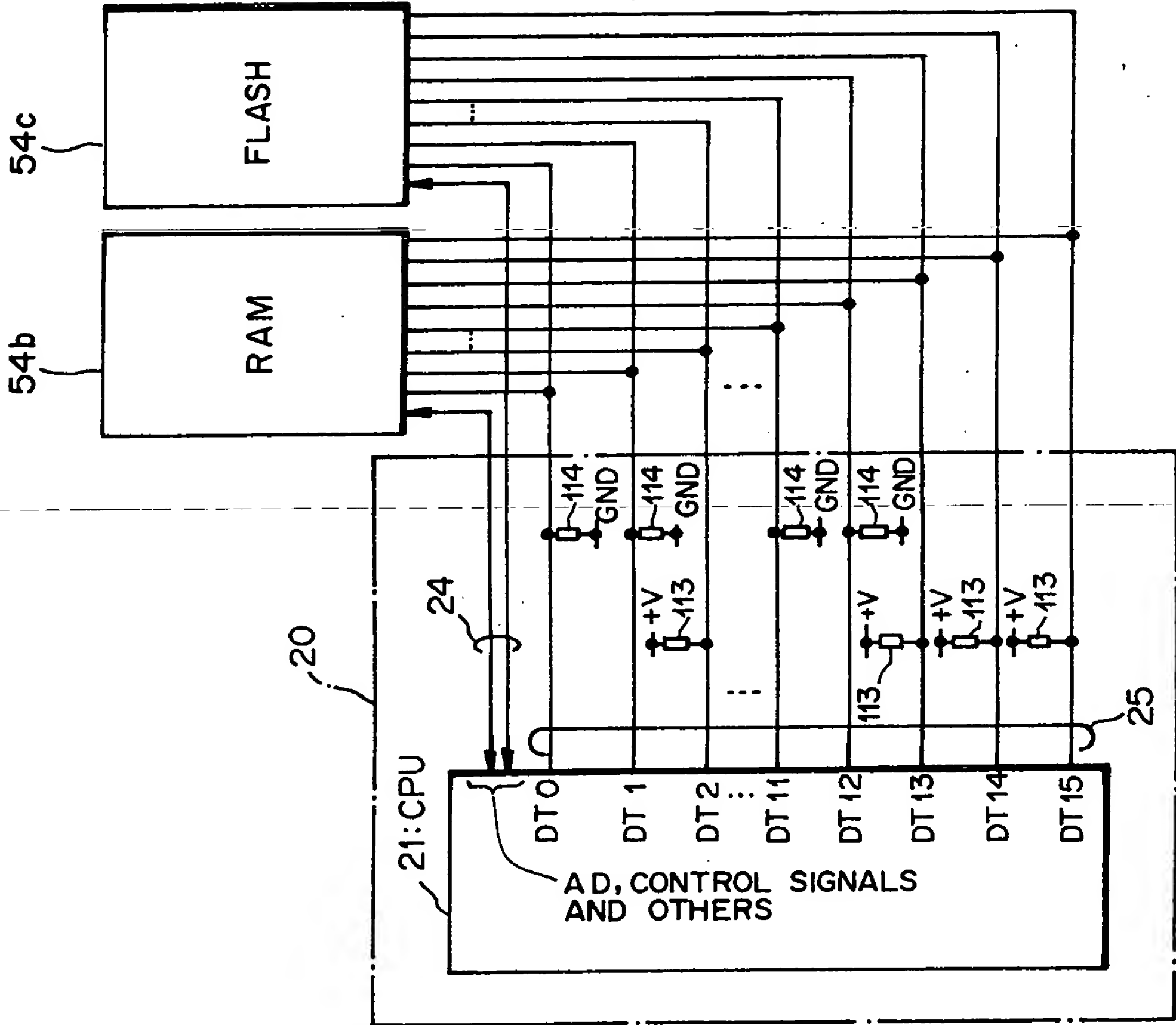
FIG. 23



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

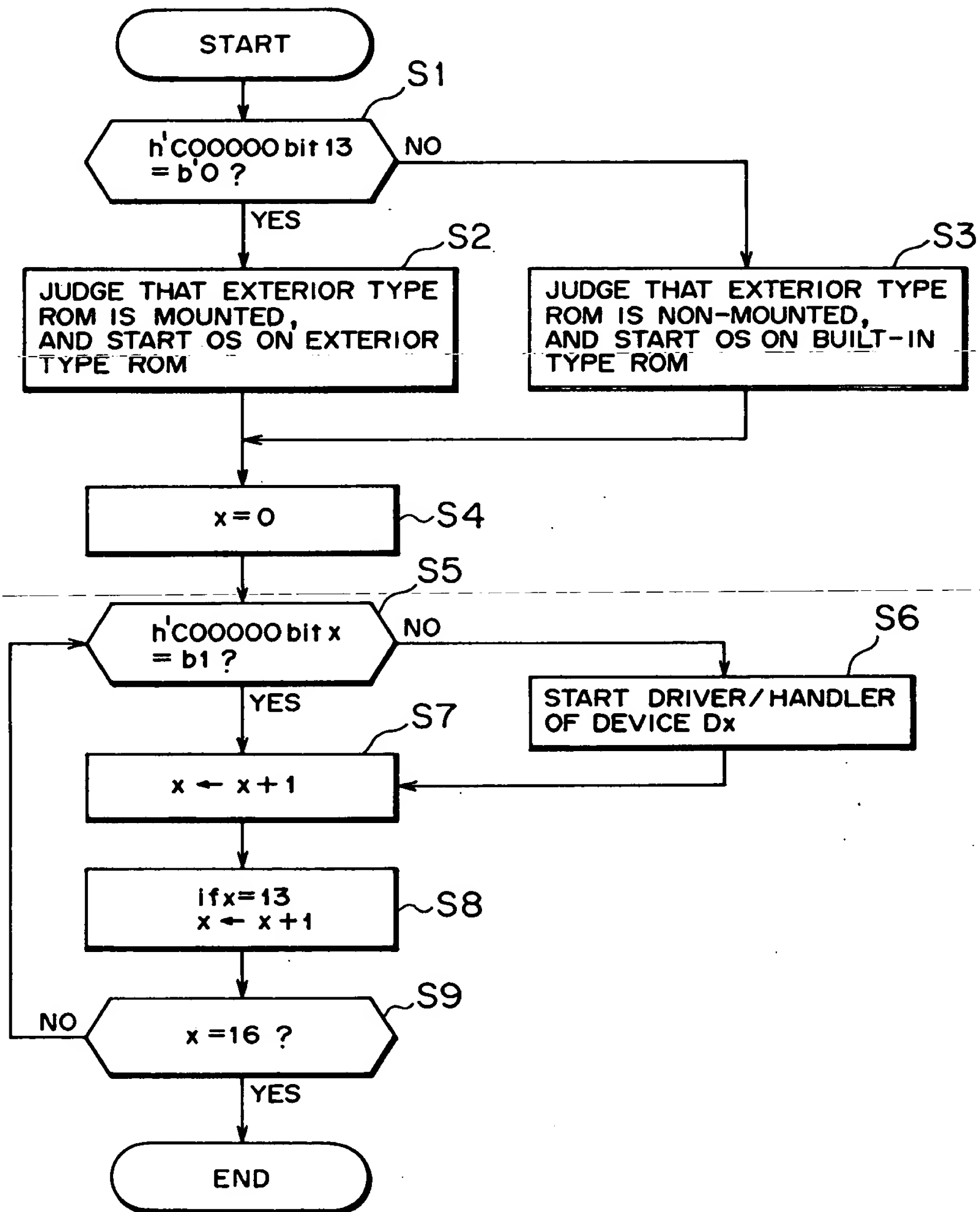
668180 8029250

FIG. 24



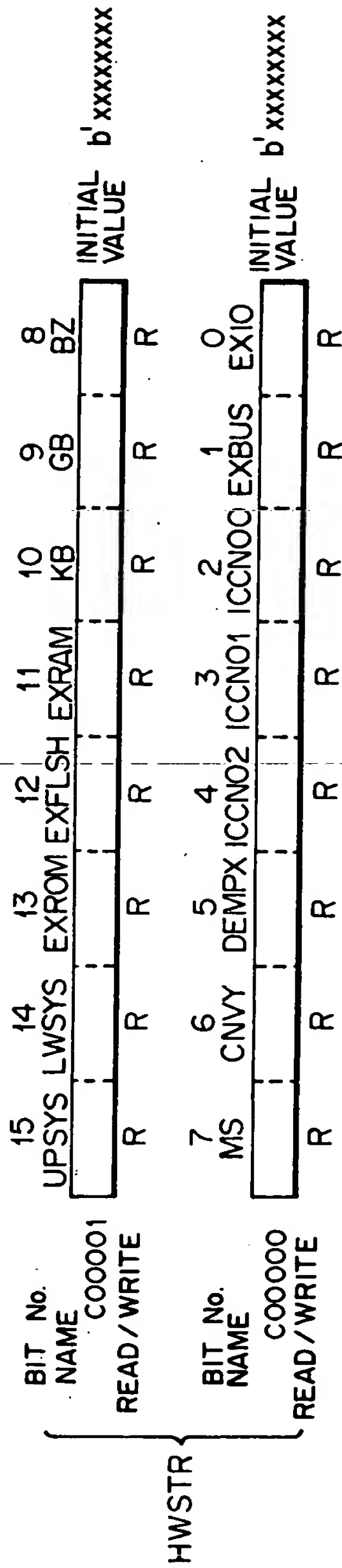
APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 25



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 26



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

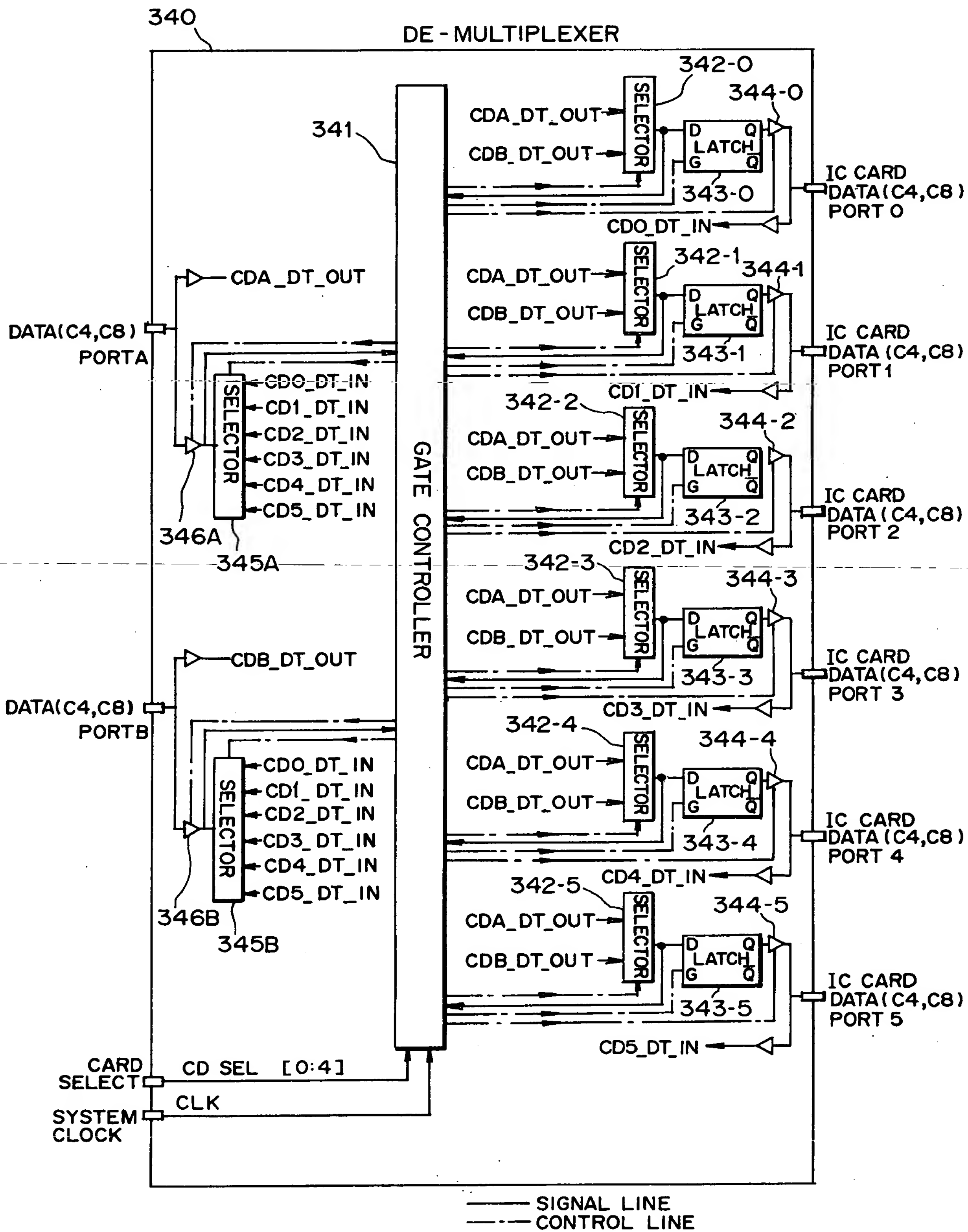
FIG.27

Bit No.	NAME	UNIT NAME	VALUE OF Bit	
			0	1
15	UPSYS	UPPER UNIT	CONNECTION	NON-CONNECTION
14	LWSYS	LOWER UNIT	CONNECTION	NON-CONNECTION
13	EXROM	EXTERIOR TYPE ROM	CONNECTION	NON-CONNECTION
12	EXFLSH	EXTERIOR TYPE FLASH	CONNECTION	NON-CONNECTION
11	EXRAM	EXTERIOR RAM	CONNECTION	NON-CONNECTION
10	KB	KEYBOARD	CONNECTION	NON-CONNECTION
9	GB	GREEN BUTTON	CONNECTION	NON-CONNECTION
8	BZ	BUZZER	CONNECTION	NON-CONNECTION
7	MS	MS READER	CONNECTION	NON-CONNECTION
6	CNVY	CONVEYER	CONNECTION	NON-CONNECTION
5	DEMPX	CARD SWITCH	CONNECTION	NON-CONNECTION
4	ICCN02	NUMBER OF IC CARD TO BE CONNECTED	SEE FIG.28	
3	ICCN01			
2	ICCN00			
1	EXBUS	EXTENDED BUS	CONNECTION	NON-CONNECTION
0	EXIO	EXTENDED I/O	CONNECTION	NON-CONNECTION

FIG.28

DEMPX	ICCN02	ICCN01	ICCN00	NUMBER OF IC CARD TO BE CONNECTED
X	0	0	0	0
X	0	0	1	1
X	0	1	0	2
0	0	1	1	3
0	1	0	0	4
0	1	0	1	5
0	1	1	0	6

# FIG. 29



# FIG. 30

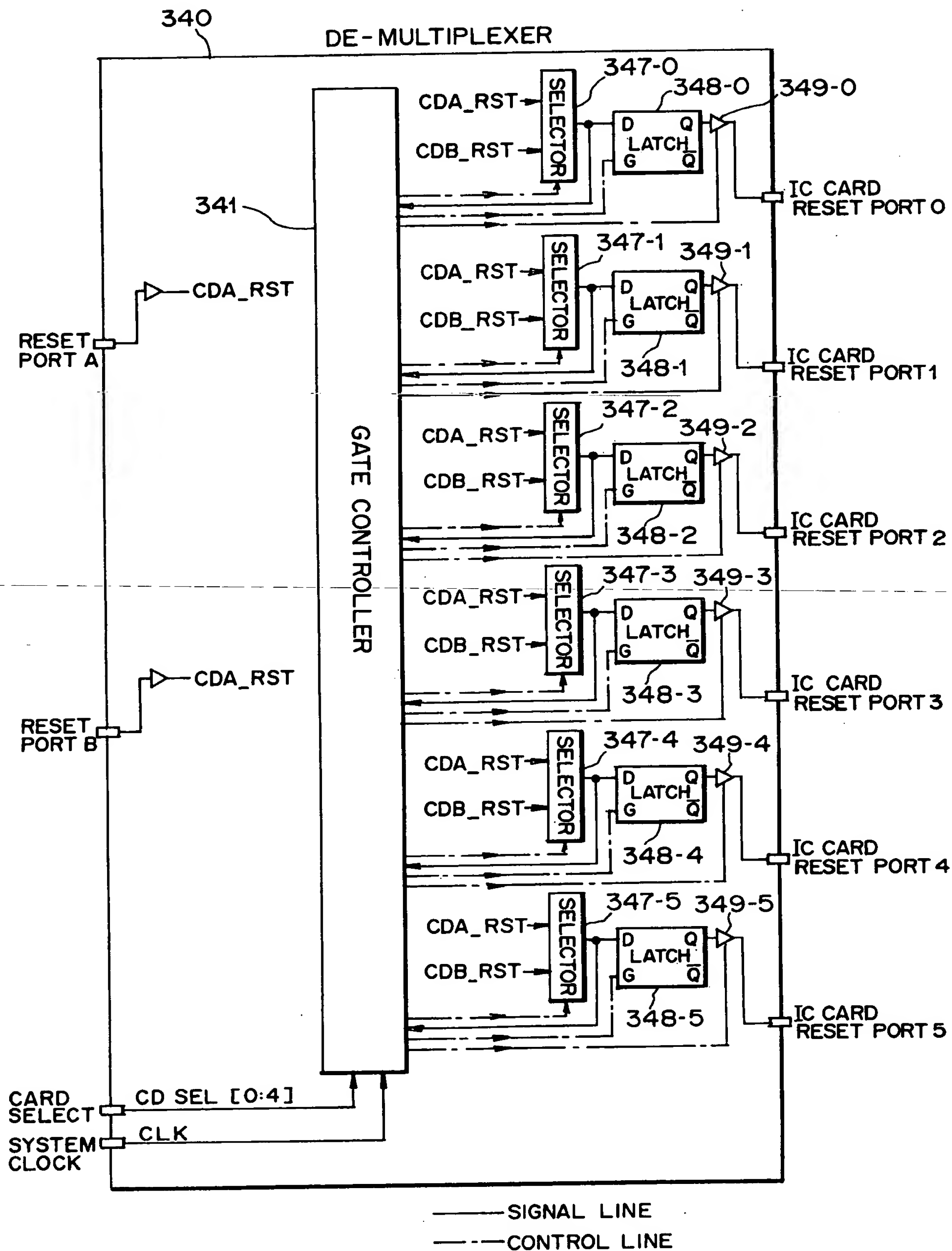


FIG. 31

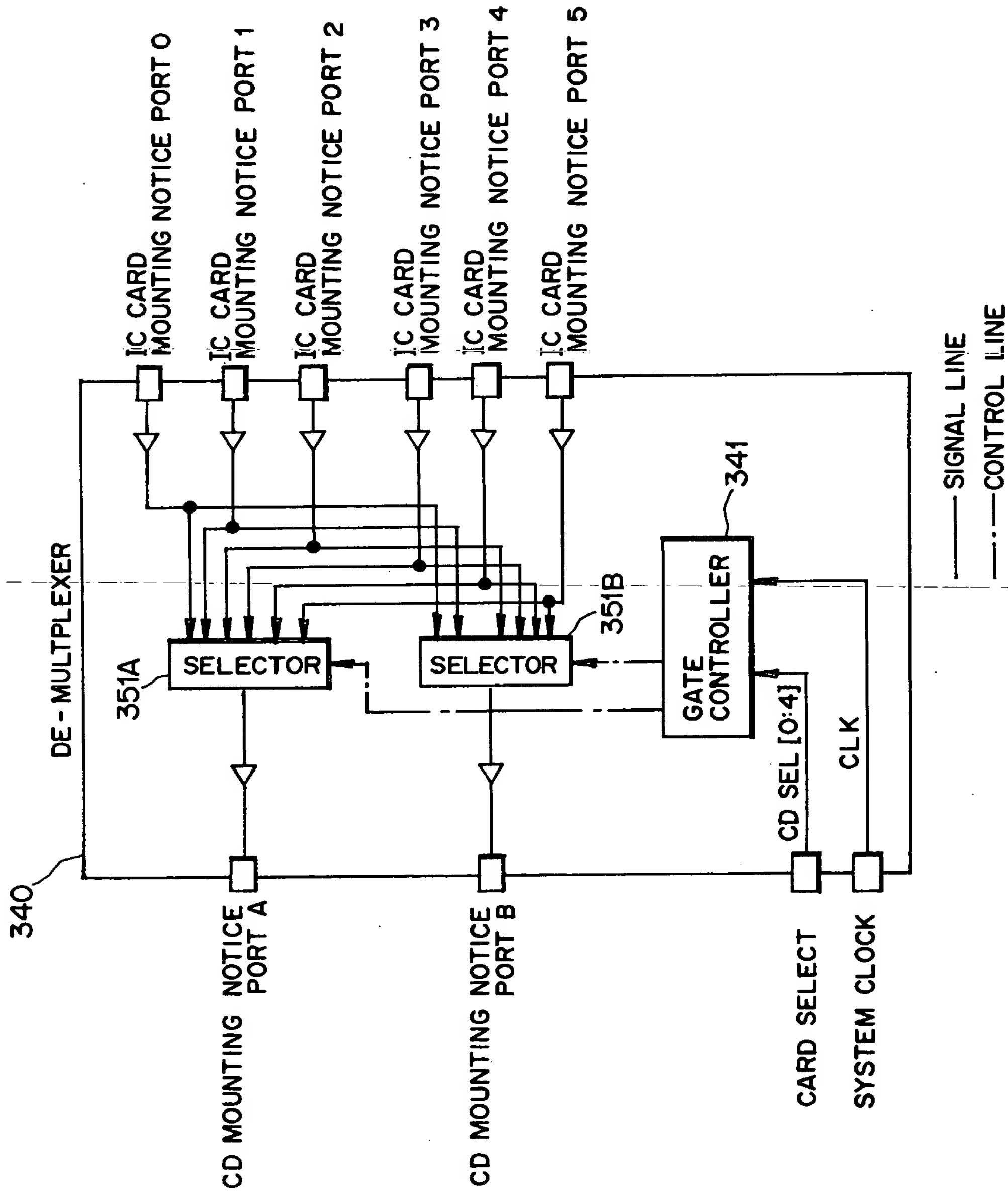




FIG. 32

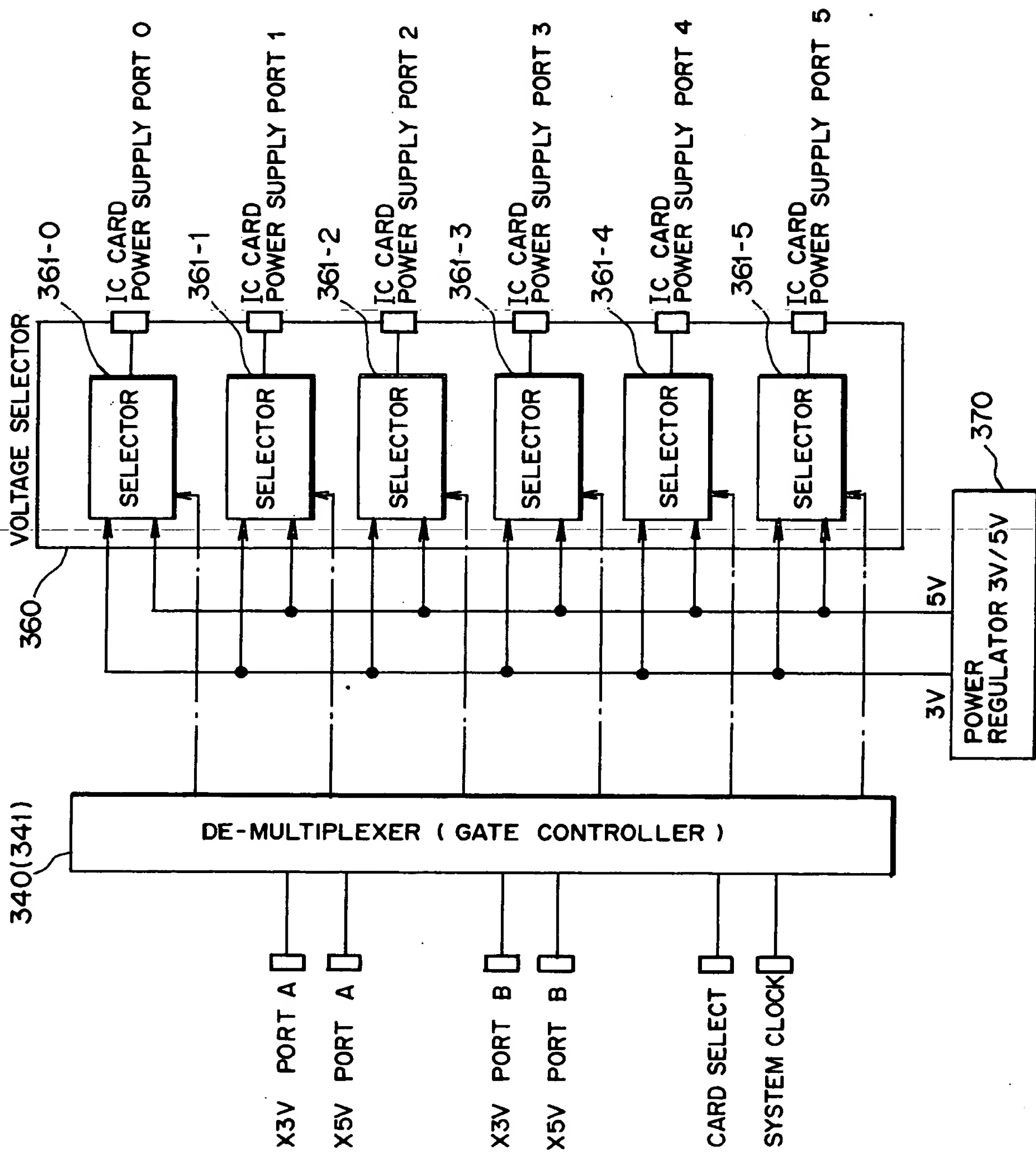


FIG. 33

Bit No.	7	6	5	4	3	2	1	0	INITIAL
NAME				CDSEL 4	CDSEL 3	CDSEL 2	CDSEL 1	CDSEL 0	VALUE
CDSEL 002080	-	-	-						bxxx00000
READ / WRITE	-	-	-	R/W	R/W	R/W	R/W	R/W	

FIG. 34

CDSEL 4	CDSEL 3	CDSEL 2	CDSEL 1	CDSEL 0	PORT A	PORT B
0	0	0	0	0	DEMULTIPLEXER RESET	
0	0	0	0	1	CARD 0 SELECTION	CARD 1 SELECTION
0	0	0	1	0	CARD 0 SELECTION	CARD 2 SELECTION
0	0	0	1	1	CARD 0 SELECTION	CARD 3 SELECTION
0	0	1	0	0	CARD 0 SELECTION	CARD 4 SELECTION
0	0	1	0	1	CARD 0 SELECTION	CARD 5 SELECTION
0	0	1	1	0	CARD 1 SELECTION	CARD 0 SELECTION
0	0	1	1	1	CARD 1 SELECTION	CARD 2 SELECTION
0	1	0	0	0	CARD 1 SELECTION	CARD 3 SELECTION
0	1	0	0	1	CARD 1 SELECTION	CARD 4 SELECTION
0	1	0	1	0	CARD 1 SELECTION	CARD 5 SELECTION
0	1	0	1	1	CARD 2 SELECTION	CARD 0 SELECTION
0	1	1	0	0	CARD 2 SELECTION	CARD 1 SELECTION
0	1	1	0	1	CARD 2 SELECTION	CARD 3 SELECTION
0	1	1	1	0	CARD 2 SELECTION	CARD 4 SELECTION
0	1	1	1	1	CARD 2 SELECTION	CARD 5 SELECTION
1	0	0	0	0	CARD 3 SELECTION	CARD 0 SELECTION
1	0	0	0	1	CARD 3 SELECTION	CARD 1 SELECTION
1	0	0	1	0	CARD 3 SELECTION	CARD 2 SELECTION
1	0	0	1	1	CARD 3 SELECTION	CARD 4 SELECTION
1	0	1	0	0	CARD 3 SELECTION	CARD 5 SELECTION
1	0	1	0	1	CARD 4 SELECTION	CARD 0 SELECTION
1	0	1	1	0	CARD 4 SELECTION	CARD 1 SELECTION
1	0	1	1	1	CARD 4 SELECTION	CARD 2 SELECTION
1	1	0	0	0	CARD 4 SELECTION	CARD 3 SELECTION
1	1	0	0	1	CARD 4 SELECTION	CARD 5 SELECTION
1	1	0	1	0	CARD 5 SELECTION	CARD 0 SELECTION
1	1	0	1	1	CARD 5 SELECTION	CARD 1 SELECTION
1	1	1	0	0	CARD 5 SELECTION	CARD 2 SELECTION
1	1	1	0	1	CARD 5 SELECTION	CARD 3 SELECTION
1	1	1	1	0	CARD 5 SELECTION	CARD 4 SELECTION
1	1	1	1	1	LATCH ALL OUTPUTS	

53  
FIG.

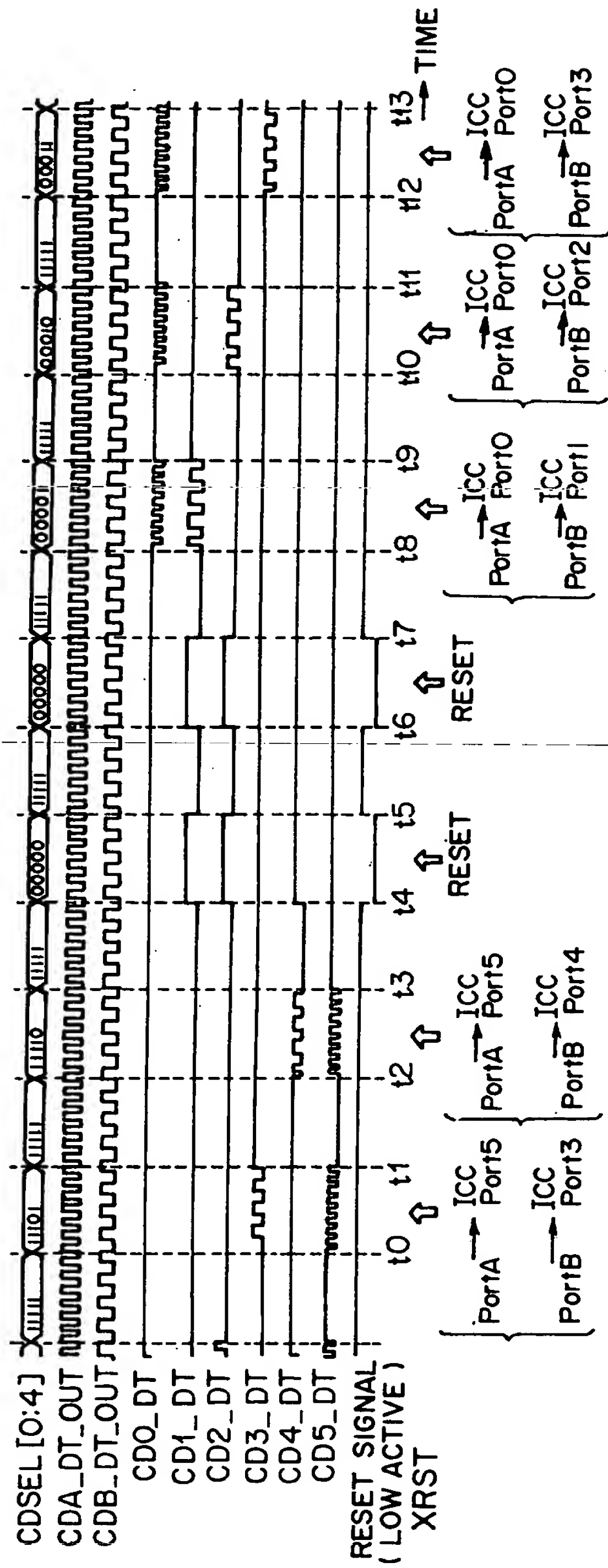


FIG. 36

